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FROM: Thomas Chan**DATE:** April 19, 2007

Number of pages with cover page:	14	Originals Will Not Follow
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Preparer of this slip has confirmed that facsimile number given is correct: 12923/lca1**Comments:****INITIAL FILING**

Examiner: S. Lo
 Art Unit: 2128
 U.S. Patent Application Serial No.: 10/772,434
 Filing Date: February 6, 2004
 Inventor(s): Ramachandran KRISHNASWAMY et al.
 Title: METHOD AND STRUCTURE TO DEVELOP A TEST PROGRAM FOR
 SEMICONDUCTOR INTEGRATED CIRCUITS
 Docket No.: 333772000800

Papers enclosed herewith:

Transmittal (1 page)
 Fee Transmittal + duplicate copy for fee processing (2 pages)
 Petition for Extension of Time – One Month (1 page)
 Amendment (9 pages)

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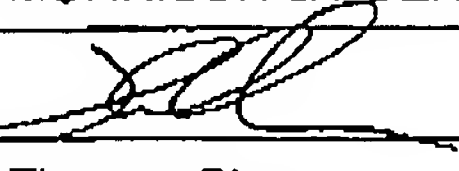
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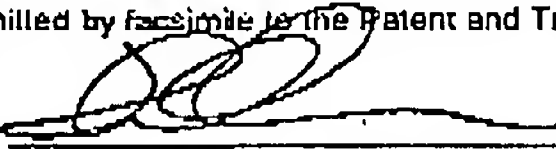
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/772,434	
	Filing Date	February 6, 2004	
	First Named Inventor	Ramachandran KRISHNASWAMY	
	Art Unit	2128	
	Examiner Name	S. Lo	
Total Number of Pages in This Submission	13	Attorney Docket Number	333772000800

ENCLOSURES (Check all that apply)		
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